

Master of Science
Computer Engineering (Technische Informatik)
MScTI

Description of the course modules



Heidelberg University
Department of Physics and Astronomy

Version 1.11

Name of university	Heidelberg University
Name of department	Institute of Computer Engineering
Name of degree course	Master of Science Computer Engineering
Format of studies	Full time, part time
Type of degree course	Consecutive
Date of version	17. 01. 2018
Prescribed period of study	Two years, i. e. four semester
Location	Heidelberg
Total number of creditpoints	120
University places	Unlimited
Target group	<p> Holders of Bachelor of Science, Magister, Staatsexamen, Diploma or equivalent final degree of at least 6 semester study having majored in computer science or in Mathematics, Natural Sciences or Engineering with attestable lectures (recommended 24 CP) imparting knowledge in computer science. </p>

Preamble

Quality Objectives at Heidelberg University for Study Programmes and Teaching

Having regard to its mission statement and constitution, Heidelberg University's degree courses have subject-related, interdisciplinary and occupational objectives. They aim to provide a comprehensive academic education equipping graduates for the world of work.

Consequently, the following competence profile has been drawn up for inclusion in module handbooks as a profile of skills valid for all disciplines. It shall be applied to the specific objectives of the individual courses and then implemented in their curricula and modules.

The main points of the competence profile:

- developing subject-related skills with a pronounced research orientation
- developing the ability to engage in transdisciplinary dialogue
- developing practice-related problem-solving skills
- developing personal and social skills
- promoting the willingness to assume social responsibility on the basis of the skills acquired

Disciplinary and Interdisciplinary Quality Objectives of the Master Programme "Computer Engineering"

The research oriented master programme in Computer Engineering (MScTI) at Heidelberg University is organized by the Institute of Computer Engineering and the Department of Physics and Astronomy. Its educational objective is to qualify students for a research or development oriented professional career in the field of Computer Engineering as well as for participation in PhD programs. The students should get a thorough understanding of possible approaches and solutions and should be able to assess their advantages and drawbacks so that they can choose a good solution for a given problem. They should realize when a solution is inappropriate or suboptimal and should be able to devise novel approaches / solutions. The MScTI emphasizes on practical skills so that the students can use the available tools and methods (software, mathematics) to develop working solutions efficiently.

Students can choose one of three specializations, which are sub-fields of 'computer engineering' (Application Specific Computing, Microelectronics, Intelligent Autonomous Systems). Each specialization consists of a set of modules on an advanced level which cover the field to a large extent. By following a sufficient number of modules in such a specialization, students reach the state-of-the art in the area to become fully competitive.

After having completed the research phase (seminar, student research project, master thesis), students have obtained the ability to do research independently, to document and publish research work. They deepen their knowledge on scientific methods, information engineering, hardware and software, interdisciplinary system thinking, experience in practical applications as well as the communication competence and the ability to work in teams.

Possible career options are in the practical development of hardware systems for data acquisition and for fast data processing, the efficient solution of compute-intensive tasks on modern, high performance, heterogeneous hardware, the design of analogue or digital microelectronics circuits, the design and operation of robotic systems, etc.

Subject-related Qualification Objectives

After completing the master programme Computer Engineering the graduates are able to program parallel systems with shared and distributed memory and use the learned structures to develop new architectures of parallel computers. They acquire a deep understanding of nonlinear dynamical systems in order to design simple nonlinear control systems. Additionally, they understand concepts about the functionality and programming of microprocessors and peripheral circuits as well as reconfigurable architectures and use these concepts to implement an own circuit respectively to implement and to program a sample embedded FPGA platform.

Transdisciplinary Qualification Objectives

Master's graduates in Computer Engineering possess the required skills to work independently with a variety of software tools for various special applications and to choose the appropriate one to solve problems. They are able to apply structured working methods and can organize complex professional projects. Also they have a basic understanding about legal aspects of founding and running a company, financial aspects of founding and running a business and are able to apply marketing strategies and tools.

Overview of the course modules

Module	Module Coordinator	ECTS
Fundamentals		
▪ Parallel Computer Architecture (compulsory)	Brüning	6
▪ System Theory (compulsory)	Badreddin	6
▪ C++ Practice	Strzodka	6
▪ Electronics	Wurz	6
▪ Introduction to High Performance Computing	Fröning	6
▪ Microcontroller Based Embedded Systems	Wurz	6
▪ Reconfigurable Embedded Systems	Kugel	6
Soft Skills		
▪ Tools	all	4
▪ Entrepreneurship	extern	6
Main Subject / Specialization		
▪ Components, Basic Circuits & Simulation	Fischer	6
▪ Full Custom VLSI Design	Fischer	6
▪ Digital Hardware Design	Brüning	6
▪ Digital Semi Custom Design Flow	Brüning	6
▪ Functional Verification	Brüning	6
▪ Advanced Analogue Building Blocks	Fischer	6
▪ Silicon Sensors & Readout Electronics	Fischer	6
▪ GPU Computing	Fröning	6
▪ Accelerator Practice	Strzodka	6
▪ Parallel Algorithm Design	Strzodka	6
▪ Advanced Parallel Algorithms	Strzodka	6
▪ Advanced Parallel Computing	Fröning	6
▪ FPGA Coprocessors	Kugel	6
▪ High Performance Interconnection Networks	Brüning	6
▪ Parallel Algorithms, Application Computing	Bastian	8
▪ Parallel Solution of Large Linear Systems	Bastian	8
▪ Physics of Imaging	Jähne	4
▪ Digital Image Processing	Jähne	8
▪ Modern Image Sensors	Jähne	2
▪ Pattern Recognition	Hamprecht	8
▪ Advanced System Theory	Badreddin	6
▪ Design of Autonomous Systems	Badreddin	6
▪ Digital Control	Badreddin	6
▪ Data Acquisition	Badreddin	6
▪ Design of Reliable and Dependable Systems	Badreddin	6

Structure of Courses

All in all, the following modules have to be completed successfully (120 CP):

- 3 modules from „Fundamentals“ 18 CP
- 5 modules from „Main Subject / Specialization“ 30 CP
- 2 modules from „Free Courses“ 12 CP
- 2 or more modules from „Soft Skills“ 12 CP
- seminar 4 CP
- student research project 14 CP
- master thesis with final colloquium 30 CP

Fundamentals

The following modules from “Fundamentals” are compulsory and have to be completed (mandatory modules):

- Parallel Computer Architecture
- System Theory

As an elective you can choose any other subject listed above in “Fundamentals”.

Main Subject / Specialization

In principle, advanced students can choose their in-depth modules freely according to the examination rules / regulations. We recommend, however, following one of the model curricula completing an exceptional specialization in a certain field of Computer Engineering. When completing a sufficient number of modules in such a specialization during your studies, this specialization will be documented explicitly in your Master Grade Report.

This is a list of model curricula and the modules required in each case:

1. Microelectronics

3 compulsory modules:

- Components, Basic Circuits & Simulation
- Digital Hardware Design
- Full Custom VLSI Design

2 elective modules from:

- Advanced Analog Building Blocks
- Digital Semicustom Design Flow
- Electronics
- Functional Verification
- Microcontroller Based Embedded Systems
- Reconfigurable Embedded Systems
- Silicon Sensors & Readout Electronics

2. Application Specific Computing

3 compulsory modules:

- GPU Computing
- Parallel Algorithm Design
- Reconfigurable Embedded Systems

2 elective modules from:

- Accelerator Practice
- Advanced Parallel Algorithms
- Advanced Parallel Computing
- C++ Practice
- Electronics
- FPGA Coprocessors
- High Performance Interconnection Networks
- Introduction to High Performance Computing
- Microcontroller Based Embedded Systems
- Parallel Algorithms, Application Computing
- Parallel Solution of Large Linear Systems

3. Intelligent Autonomous Systems

3 compulsory modules:

- Advanced System Theory
- Design of Autonomous Systems
- Digital Control

2 elective modules from:

- Data Acquisition
- Design of Reliable and Dependable Systems
- Electronics
- Digital Image Processing
- Microcontroller Based Embedded Systems
- Parallel Solution of Large Linear Systems
- Pattern Recognition
- Reconfigurable Embedded Systems
- Silicon Sensors & Readout Electronics

Free Courses

The aim of the Free Course is to broaden ones expertise. The lectures can be chosen from the course catalogue of the Heidelberg University. To be approved as a Free Course in the MScTI, the lecture must meet the following three conditions:

- it is graded,
- the sum of credit points awarded is 12 CP or more,
- it reasonably contributes to a broadening of the expertise for this study program.

In case of doubt, the Dean of Studies makes the decision on the approval.

It is also possible to select Fundamentals or Specializations modules from the MScTI for the Free Course. When the decision on a preferred specialization is not yet made in the first semester, it is a good choice to take a second specialization as the Free Course.

Soft Skills

All in all, 12 CP must be completed in the field of soft skills, 2 of which are integrated in the seminar. For the remaining 10 CP the following courses can be chosen:

- Tools (4 CP),
- Entrepreneurship (6 CP),
- Courses from the University course program classified as soft skill courses,
- Language Courses (6 CP maximum).

Seminar

For the seminar 2 CP are allocated as soft skills in addition to 4 CP for the professional contents.

Seminar: 4 CP (professional contents) + 2 CP (soft skills)

Overview of the course modules

Fundamentals:

Code: MScTI_PCA		Course Title: Parallel Computer Architecture		
Lecturer: Prof. Dr. U. Brüning		Type: Lecture with exercises / lab / ...		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2 hours/week) • Exercise / Lab: practical programming exercises on parallel computer system (2 hours/week) 				
Objectives: The students... <ul style="list-style-type: none"> • understand the concepts and principles of parallel processing and the underlying hardware structures, so that they are able to program parallel systems with shared and distributed memory, • use the learned structures to develop new architectures of parallel computers. 				
Contents: <ul style="list-style-type: none"> • Concepts of Parallel Processing • SIMD-Architectures • MIMD-Architectures • Shared Memory • Distributed Memory • Communication and Synchronization • Multithreading • Taxonomy of Interconnection Networks • Point-to-Point INs • Switched INs, Shuffles, Crossbars, Routing, Latency • Communication Protocols • Virtual Shared Memory • Dataflow Architectures 				
Prerequisites: none		Recommended Knowledge: basic knowledge of Computer Architecture		
Literature: a reading list will be provided in the script The script will be accessible on the web site of the Computer Architecture Chair				
Form of Testing and Examination: 30' oral exam at the end of the semester At least 50% of the exercises must be passed.				

Code: MScTI_SYSTHEO		Course Title: System Theory		
Lecturer: Prof. Dr. sc. techn. E. Badreddin		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture: 2 hours/week • Exercise with homework: 2 hours/week 				
Objectives: After completing this course the students will be able to: <ul style="list-style-type: none"> • describe nonlinear phenomena and nonlinear dynamical systems, • analyze nonlinear systems by using phase plane, describing functions and Lyapunov theory, • design simple nonlinear control systems and • apply the methods to simple practical examples. 				
Contents: <ul style="list-style-type: none"> • Introduction to nonlinear systems, • Nonlinear phenomena (limit cycles, bifurcations and chaos), • Phase plane analysis • Describing function • Lyapunov stability • Design of relay feedback control, time optimal control and sliding control 				
Prerequisites: none		Recommended Knowledge: Theory of linear systems (Signals and Systems 1)		
Literature: J.J.-E. Slotine and W. Li, Applied Nonlinear Control, Prentice-Hall, 1991. H. K. Khalil, Nonlinear Systems, Third Edition, Prentice Hall, 2000.				
Form of Testing and Examination: 30' oral exam				

Code: MScTI_ADVCPP		Course Title: C++ Practice		
Lecturer: Prof. Dr. R. Strzodka		Type: Lecture with exercises and project		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture 2 hours/week • Exercise 1 hour/week on average plus homework • Project 1 hour/week on average plus homework 				
Objectives: Students are able to ... <ul style="list-style-type: none"> • apply all major features of modern C++, • design better programs following guidelines for an effective programming style, • use and combine different programming patterns. 				
Contents: <ul style="list-style-type: none"> • A tour of modern C++ from start to end • Immediate use of new functionality since C++11, e.g. constexpr, move refs and ctors, initializer lists, lambdas, variadic templates • How to select among the language features • Clear and effective programming style • Intensive exercises with practical applications of discussed C++ feature 				
Prerequisites: none		Recommended Knowledge: Understanding of all basic C++ concepts such as references, classes, inheritance, overloading, templates, STL		
Literature: <ul style="list-style-type: none"> • Bjarne Stroustrup: A Tour of C++, Addison-Wesley, 2014 • Bjarne Stroustrup: The C++ programming language, 4th ed, Addison-Wesley, 2013 				
Form of Testing and Examination: 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the lecturer.				

Code: MScTI_ELEC		Course Title: Electronics		
Lecturer: A. Wurz		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture on "Electronics" (3hours/week) • Exercise with homework (1 hours/week) 				
Objectives: After this course the students will be able to: <ul style="list-style-type: none"> • analyze the application of active and passive components, • understand the methods of circuit design, • design simple electronic circuits and apply the methods to practical examples. 				
Contents: <ul style="list-style-type: none"> • Resistors, capacitors, inductivities • Diodes (rectifiers, switches) • Transistors (amplifier, switches) • Field-effect transistors (JFET, MOSFET) • Operational amplifier (amplifier, analog filter) • Oscillators (LC oscillators, crystal oscillators) • Phase-locked loop, Laplace transformation • Power supply circuits • Transmission of analog and digital signals • Analog to digital conversion • Simulation of circuits • Techniques of electronic design 				
Prerequisites: none		Recommended Knowledge: none		
Literature: Horowitz and Hill: The Art of Electronics; Herrmann Hirsch: Elektronik; U. Tietze, Ch. Schenk: Halbleiterschaltungstechnik				
Form of Testing and Examination: To be defined by lecturer before beginning of course				

Code: MScTI_INTROHPC		Course Title: Introduction to High Performance Computing		
Lecturer: JProf. Dr. H. Fröning		Type: Lecture with exercise		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture (3 hours/week) • Exercise with homework (1 hours/week) 				
Objectives: Students...				
<ul style="list-style-type: none"> • know message passing and scalable programming • are familiar with the most important past and present concepts for large-scale computing problems • can design and optimize solutions for large-scale computing problems • know how to use MPI and related software tools to implement large-scale computing problems • are capable to solve large-scale computing problems with objectives including performance in terms of time and energy, and scalability in terms of time and capacity, and consolidation of compute resources 				
Contents:				
<ul style="list-style-type: none"> • The challenges of HPC • Hardware architectures • Software frameworks, in particular MPI • Short introduction to accelerated computing • Practical problems and their solutions 				
Prerequisites: none		Recommended Knowledge: Computer architecture basics, parallel programming principles, C, C++, OS basics		
Literature: Will be announced by lecturer				
Form of Testing and Examination: 15 – 30 min. oral exam or 1h written exam announced by lecturer				

Code: MScTI_MES		Course Title: Microcontroller Based Embedded Systems		
Lecturer: A. Wurz		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2h) • Exercise/ Lab work (2h) 				
Objectives: After this course the students will be able to: <ul style="list-style-type: none"> • analyse active and passive electronic components for practical applications, • understand the methods of circuit design, the functionality and programming of microprocessors and peripheral circuits so that they can build a complex microprocessor circuit themselves, • install a development environment for program development, • apply methods for debugging in microprocessor circuits. 				
Contents: <ul style="list-style-type: none"> • project management • circuit design • microcontrollers • mp3 decoder + Ethernet • power supply (linear regulators +switching regulators) • selection of components • CAD program (schematic +layout) • manufacturing boards • stuffing boards • C program development • test program • implementing + debugging • electronic construction techniques 				
Prerequisites: none		Recommended Knowledge: none		
Literature: <ul style="list-style-type: none"> • Paul Horowitz, Winfield Hill: The Art of Electronics 				
Form of Testing and Examination: To be defined by lecturer before beginning of course				

Code: MScTI_RES		Course Title: Reconfigurable Embedded Systems		
Lecturer: Dr. A. Kugel		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2 hours) / practical exercise (lab, 1 hour avg) / project (lab, 1 hour avg) / homework • Just-in-time teaching sessions (4 hours) on selected topics 				
Objectives: After completing this course students are able to ... <ul style="list-style-type: none"> • list and explain important elements and properties of embedded systems, • describe fundamental principles and components of reconfigurable technology, • apply elementary application design methodologies for microprocessors and FPGAs to implement, program and test a sample embedded FPGA platform. 				
Contents: <ul style="list-style-type: none"> • Requirements and specific properties of embedded systems • Overview on hardware components: microcontrollers, peripherals, FPGAs • Real-time issues and scheduling • FPGA design tools: HDL (incl. VHDL tutorial), simulator, debugger • System-on-Chip architecture – controller, buses and peripherals • HW/SW co-design • Embedded system software (stand-alone and real-time kernels) 				
Prerequisites: none		Recommended Knowledge: none		
Literature: <ul style="list-style-type: none"> • Peter Marwedel: Eingebettete Systeme, SpringerLehrbuch, 1. Auflage 2007 • Th. Flick, H. Liebig: Mikroprozessortechnik, SpringerLehrbuch, 4. Auflage 2004 • H. Bähring: MikrorechnerSysteme, SpringerLehrbuch, 3. Auflage 2002 • Karim Yaghmour: Building Embedded Systems, O'Reilly, April 2003 				
Form of Testing and Examination: 50% score on exercises plus either oral exam (15min) or project (see below). Available option announced at start of course. Project exam: autonomous elaboration of project task assigned by lecturer. Successful completion requires all of: operational design/program, written report (5 pages), presentation (10 minutes) with colloquium, statement of unaided work.				

Soft Skills:

Code: MScTI_TOOLS		Course Title: Tools		
Lecturer: several, changing		Type: Lecture with exercises		
Credit Points: 4	Workload: 120 h	Teaching Hours: 4 / week	Language German	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2 h / week) • Supervised Exercises (2 h / week) 				
Objectives: Students <ul style="list-style-type: none"> • have an overview of the functionalities of various software tools suited to accomplish frequent tasks, like the creation of drawings and illustrations, programming, solving of mathematical problems, analysis and visualization of data, search for literature or working in a team. • are able to improve their work flows by choosing an appropriate tool • are aware that application of a suited tool improves their working quality and efficiency • are able to deepen their knowledge and skill in the presented tools on the basis of the introductions given 				
Contents: <p>Subjects are chosen from a pool of possibilities as a function of interests and need of students. The list is regularly adapted to new developments</p> <ul style="list-style-type: none"> • introduction to Linux • version control tools (git, svn,..) • introduction to python • mathematical software (Mathematica) • data evaluation and plotting (gnuplot, root) • software documentation tools (doxygen) • 2D & 3D drawing, construction and visualization (PovRay, OpnSCAD, PostScript, pdf) • styles and templates (powerpoint, word) • introduction to Latex • team work • project planning • literature search 				
Prerequisites: none		Recommended Knowledge: none		
Literature: announced by lecturer				
Form of Testing and Examination: Regular participation (maximum 2 courses missed without justification) and successful completion of supervised exercises				

Code: MScTI_ES		Course Title: Entrepreneurship		
Lecturer: tbd (external)		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2 hours/week) • Exercise (2 hours/week) 				
Objectives: After completing this course students will be able to: <ul style="list-style-type: none"> • use innovation methods like Design Thinking, Rapid Prototyping and Business Model Innovation to develop tangible solutions for real life problems • present business cases in short presentations (pitches) to stakeholders and investors • develop basic marketing strategies to find first customers for their products • apply the legal framework to found their own company 				
Contents: <ul style="list-style-type: none"> • Design Thinking • Rapid Prototyping • Business Model Innovation • Presentation of business cases • finance • marketing • patent law and copyright • legal forms of companies 				
Prerequisites: none		Recommended Knowledge: none		
Literature: Eric Ries: Lean Startup, Redline Verlag, 2012 Alexander Osterwalder: Business Model Generation, Campus Verlag, 2011 Steve Blanck: The Startup Owners's Manual, K & S Ranch, 2012 Ash Maurya: Running Lean, O'Reilly, 2013				
Form of Testing and Examination: Presentation of results				

Main Subject / Specialization:

Code: MScTI_ANASIM		Course Title: Components, Basic Circuits & Simulation		
Lecturer: Prof. Dr. P. Fischer		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture (2h) • Practical exercise with homework (2h) 				
Objectives: Students...				
<ul style="list-style-type: none"> • can design simple analog circuits by combining elementary building blocks • can predict the properties (gain, frequency behavior) of simple circuits and give analytical approximate expressions for gain, bandwidth, output resistance, etc. • can use analogue simulators to analyze circuits in the time and frequency domain • know what an operation point is, how it affects circuit behavior and how it can be set • can relate the geometry and operation point of transistors to their small- and large signal properties 				
Contents:				
<ul style="list-style-type: none"> • Diode and transistor operation principle • Modelling of Diode und MOS, large / small signal models • Voltage and current sources, Thevenin equivalent • Component and circuit description with complex variables • Bode plot, transfer function • Analogue simulation (dc, ac, transient) • Basic circuits: current mirror, gain stage, cascode, source follower, differential pair • Practical exercises with professional simulation tools 				
Prerequisites: none		Recommended Knowledge: Introduction to physics		
Literature:				
<ul style="list-style-type: none"> • P. R. Gray, P. J. Hurst, S. H. Lewis, R. G. Meyer: Analysis and Design of Analog Integrated Circuits, Wiley & Sons, New York, 1993 • D. A. Johns, K. Martin: Analog Integrated Circuit Design, Wiley & Sons, 1997 				
Form of Testing and Examination: To be defined by lecturer before beginning of course				

Code: MScTI_ANADESIGN		Course Title: Full Custom VLSI Design		
Lecturer: Prof. Dr. P. Fischer		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture (2h) • Practical exercise with homework (2h) 				
Objectives: Students...				
<ul style="list-style-type: none"> • can carry out the complete design process from a circuit idea to a final, checked layout, • understand how design rules are related to semiconductor properties or manufacturing issues, • are able to practically carry out a mixed mode simulation, • are able to extract parasitic values and perform a simulation with these parasitics, • can program simple automatized scripts using SKILL. 				
Contents:				
<ul style="list-style-type: none"> • Semiconductor manufacturing • Technology & design rules, technology files • Layout of components, rules, matching • Design Rule Check • Extraction, Layout versus Schematic Check • ESD and Antenna rules, latchup • Test equipment & test procedures • Script programming using SKILL • Parasitic extraction & simulation • Mixed Mode simulation 				
Prerequisites: none		Recommended Knowledge: MScTI_ANASIM		
Literature:				
<ul style="list-style-type: none"> • Lecture script available online 				
Form of Testing and Examination: Design (schematic entry, simulation and layout) of a simple circuit with a short presentation.				

Code: MScTI_DIGHD		Course Title: Digital Hardware Design		
Lecturer: Prof. Dr. U. Brüning		Type: Lecture with exercises / lab / ...		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture (2 hours/week) • Exercise: design and simulation of digital hardware with EDA software (2 hours/week) 				
Objectives: The students ...				
<ul style="list-style-type: none"> • understand the concepts and principles of hardware design and the methodology for design and verification of hardware structures that means concretely that they are able to use their acquired knowledge to design new and efficient hardware and they can simulate and verify the developed designs. 				
Contents:				
<ul style="list-style-type: none"> • Introduction to the principles of hardware design • use of Hardware Description Languages like Verilog HDL. • design of combinational and sequential logic. • overall design flow for Integrated Circuits • Design descriptions • Design elements • Simulation • Verification of Hardware 				
Prerequisites: none		Recommended Knowledge: basic knowledge of Digital Circuit Design		
Literature: a reading list will be provided in the script The script will be accessible on the web site of the Computer Architecture Chair				
Form of Testing and Examination: 30' oral exam at the end of the semester At least 50% of the exercises must be passed.				

Code: MScTI_DIGDF		Course Title: Digital Semi Custom Design Flow		
Lecturer: Prof. Dr. U. Brüning		Type: Lecture with exercises / lab / project ...		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture ... (2 hours/week) • Exercise / Project: backend processing for ASICs with EDA software (2 hours/week) 				
Objectives: The students ... <ul style="list-style-type: none"> • deepen their knowledge of the methodology for semi-custom ASIC design, • are able to use their acquired knowledge to design very complex chips, • can run the complete backend design process for modern chip technology. 				
Contents: <ul style="list-style-type: none"> • Advanced methods for design of application specific ICs • Synthesis of complex hardware systems • Static Timing Analysis (STA) • Place&Route of modules and standard cells • Signal integrity analysis • Design rule checks • Generation of mask data • The SEED-2002 agreement between Cadence Design Systems and the University of Heidelberg allows our students to work and learn with the most modern EDA tools that are usually only used in chip industry. 				
Prerequisites: none		Recommended Knowledge: deeper knowledge of Digital Hardware Design		
Literature: a reading list will be provided in the script The script will be accessible on the web site of the Computer Architecture Chair				
Form of Testing and Examination: 30' oral exam at the end of the semester At least 50% of the exercises and the chip project must be passed.				

Code: MScTI_DIGVERI		Course Title: Functional Verification		
Lecturer: Prof. Dr. U. Brüning		Type: Lecture with exercises / lab / ...		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture ... (2 hours/week) • Exercise ... (2 hours/week) 				
Objectives: The students... <ul style="list-style-type: none"> • understand the concepts and principles of functional verification and the methodology, • use the acquired for building verification environments, • are able to verify complex hardware designs. 				
Contents: <ul style="list-style-type: none"> • Introduction to the principles of functional verification • Simulation-Based Verification • Formal Verification • Use of Hardware Verification Languages like System Verilog • Use of Verification Methodologies like OVM • Verification Planning • Coverage Models • Assertion-Based Verification 				
Prerequisites: none		Recommended Knowledge: Experience in Digital Hardware design		
Literature: a reading list will be provided in the script The script will be accessible on the web site of the Computer Architecture Chair				
Form of Testing and Examination: 30' oral exam at the end of the semester At least 50% of the exercises must be passed.				

Code: MScTI_ANABLOCKS		Course Title: Advanced Analogue Building Blocks		
Lecturer: Prof. Dr. P. Fischer and others		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS+SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2 hours/week) • Practical exercise (2 hours/week) 				
Objectives: The students ... <ul style="list-style-type: none"> • have a broad overview of advanced circuits so that they are able to chose an appropriate approach for a given problem • get a deeper qualitative understanding of the behavior of analogue circuits, • can quantitatively analyze analogue circuits and extract important figures of merit, • know a large variety of advanced circuit topologies. 				
Contents: <p>The lecture introduces various building blocks, mathematical tools or knowledge on more advanced topics, picked from the list below as a function of student background and interest. Content in SW or SS can vary.</p> <ul style="list-style-type: none"> • Advanced transistor properties • Feedback: properties, mathematical treatment, stability, Nyquist test • Noise of components and circuits • Transfer function, impulse response, poles and zeros • Cascaded amplifiers • Advanced current mirrors • Differential circuits, common mode feedback • DACs and ADCs • Switches • Switched Capacitor Circuits 				
Prerequisites: none		Recommended Knowledge: MScTI_ANADESIGN		
Literature: <ul style="list-style-type: none"> • Razavi „Design of analog CMOS integrated circuits“ • J. Millman „Microelectronics“ 				
Form of Testing and Examination: To be defined by lecturer before beginning of course				

Code: MScTI_DET		Course Title: Silicon Detectors & Readout Electronics		
Lecturer: Prof. Dr. P. Fischer		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: English	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture (2 hours/week) • Exercise with homework (2 hours/week) 				
Objectives: The students ...				
<ul style="list-style-type: none"> • know the basic working principles of silicon detectors, so that they are able to derive important properties (speed, resolution..) • know different sensor types with their properties, so that they can choose the best detector type for a given application • know the basics on how to read out the signals and which figures of merit are of importance so that they can design / chose readout concepts for a given application 				
Contents:				
<ul style="list-style-type: none"> • Basics <ul style="list-style-type: none"> ○ Interactions of particles and photons with matter (short) ○ Semiconductors, doping, diodes, manufacturing technology ○ Spatial resolution, energy resolution, noise... • Particle Sensors <ul style="list-style-type: none"> ○ PiN Diodes, Pads, Pixel, Strips ○ DEPFETs, MAPS ○ Non-silicon materials • Photo Sensors <ul style="list-style-type: none"> ○ Quantum efficiency, spectral sensitivity, response time ○ APDs, SiPMs, CCDs, CMOS APS, others • Readout circuits <ul style="list-style-type: none"> ○ Charge amplifier, Transimpedance amplifier, bandwidth, noise ○ Readout chips for strip- and pixel detectors • Applications 				
Prerequisites: none		Recommended Knowledge: Basic knowledge in Electrodynamics, Quantum Mechanics and Solid State Physics		
Literature:				
<ul style="list-style-type: none"> • Semiconductor Devices, S. M. Sze, Wiley, ISBN 0471874248 • Semiconductor Radiation Detectors, G. Lutz, Springer, ISBN 3540648593 • Pixel Detectors, Rossi/Fischer/Rohe/Wermes, Springer, ISBN 3540283323 				
Form of Testing and Examination: To be defined by lecturer before beginning of course				

Code: MScTI_GPU		Course Title: GPU Computing		
Lecturer: JProf. Dr. H. Fröning		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture on “GPU Computing” (2 hours/week) • Exercise with homework (2 hours/week) 				
Objectives: Students...				
<ul style="list-style-type: none"> • know the factors that determine the performance of GPU programs, and are able to program GPUs to solve computing problems, • are familiar with GPU architecture and design decisions, • can design and optimize CUDA programs for compute- or memory-intensive problems, • know how to use CUDA tools to aid in programming, debugging and performance tuning, • are capable to solve compute- or memory-intensive problems using GPUs with objectives including performance in terms of time and energy, and are capable to decide when accelerators like GPUs are suitable for a given computing problem. 				
Contents:				
<ul style="list-style-type: none"> • Basics of GPU architecture and programming model • Introduction to CUDA • Performance optimization techniques • Consistency and coherence of GPUs • Introduction to OpenCL and productivity tools 				
Prerequisites: none		Recommended Knowledge: Parallel programming, C++ programming skills		
Literature: T.G. Mattson, B.A. Sanders, B.L. Massingill: Parallel Patterns for Parallel Programming, Addison Wesley 2004; D.B. Kirk, W.W. Hwu: Programming Massively Parallel Processors, Morgan-Kaufmann 2010				
Form of Testing and Examination: 15 – 30 min. oral exam or 1h written exam, announced by lecturer				

Code: MScTI_PAD		Course Title: Parallel Algorithm Design		
Lecturer: Prof. Dr. R. Strzodka		Type: Lecture with exercises and project		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture 2 hours/week • Exercise 1 hour/week on average plus homework • Project 1 hour/week on average plus homework 				
Objectives: Students are able to ... <ul style="list-style-type: none"> • exploit the available parallelism in modern CPUs, • make design decisions depending on tradeoffs in parallel algorithms, • apply and combine parallel patterns in their own programs. 				
Contents: <ul style="list-style-type: none"> • Multiple levels of parallelism • Parallel data access • Communication vs. computation • Latency vs. throughput • Work efficiency vs. step efficiency • Locality vs. parallelism • Parallel design patterns 				
Prerequisites: none		Recommended Knowledge: Basic C++		
Literature: <ul style="list-style-type: none"> • Michael McCool, Arch Robison, James Reinders: Structured Parallel Programming, Morgan Kaufmann, 2012 				
Form of Testing and Examination: 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the lecturer.				

Code: MScTI_ACC		Course Title: Accelerator Practice		
Lecturer: Prof. Dr. R. Strzodka		Type: Lecture with exercises and project		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture 2 hours/week • Exercise 1 hour/week on average plus homework • Project 1 hour/week on average plus homework 				
Objectives: Students are able to ... <ul style="list-style-type: none"> • program accelerators on a high-level with parallel patterns, • create multi-backend programs that can run on different architectures, • select efficient parallel algorithms from existing accelerator libraries. 				
Contents: <ul style="list-style-type: none"> • Overview of programming paradigms for accelerators • High level accelerator programming • Effective use of STL-like algorithm libraries • Multi-backend programming for different architectures • Libraries for dense and sparse linear algebra • Specialized libraries • Simultaneous use of multiple accelerators 				
Prerequisites: none		Recommended Knowledge: Basic C++		
Literature: <ul style="list-style-type: none"> • Will be announced by the lecturer 				
Form of Testing and Examination: 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the lecturer.				

Code: MScTI_APA		Course Title: Advanced Parallel Algorithms		
Lecturer: Prof. Dr. R. Strzodka		Type: Lecture with exercises and project		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture 2 hours/week • Exercise 1 hour/week on average plus homework • Project 1 hour/week on average plus homework 				
Objectives: Students are able to ... <ul style="list-style-type: none"> • apply advanced transformations to improve parallelism and locality, • make detailed design decisions depending on tradeoffs in parallel algorithms, • balance numerical efficiency and parallel efficiency. 				
Contents: <ul style="list-style-type: none"> • The lectures MScTI_PAD and MScTI_APA can be attended in the same semester in parallel. MScTI_PAD looks at more topics in breadth, while MScTI_APA looks at fewer topics in depth. • Most recent developments in parallel devices • On-the-fly data transformations • Data locality optimizations • Hierarchical algorithms • SIMD utilization • Precision, accuracy and numerical schemes • Numerical efficiency vs. parallel efficiency • Data representation 				
Prerequisites: none		Recommended Knowledge: Basic C++, CUDA (e.g. MScTI_GPU), and MScTI_PAD in parallel		
Literature: <ul style="list-style-type: none"> • David B. Kirk, Wen-mei W. Hwu: Programming Massively Parallel Processors, 3rd ed, Morgan Kaufmann, 2017 • More will be announced by the lecturer 				
Form of Testing and Examination: 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the lecturer.				

Code: MScTI_APC		Course Title: Advanced Parallel Computing		
Lecturer: JProf. Dr. H. Fröning		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture ... (2 hours/week) • Exercise: homework and practical programming exercises on parallel architectures (2 hours/week) 				
Objectives: The students ... <ul style="list-style-type: none"> • know principles of parallel architectures, including synchronization, consistency, and coherence, • are familiar with advanced concepts like transactional memory, relaxed consistency, and multi-threading, • know how to design and optimize complex parallel code for particular compute and synchronization problems, • are capable of solving complex computing problems using massively parallel processors, understanding the implications of architectural design decisions on performance in terms of time and energy, and reasoning about the suitability of certain processor architectures for a given computing problem. 				
Contents: <ul style="list-style-type: none"> • Principles of parallel computing • Shared memory architectures • Programming paradigms, communication and synchronization primitives • Consistency models and scalable cache coherence • Multi-/many-core and multi-threading architectures • Exemplary applications and benchmarks 				
Prerequisites: none		Recommended Knowledge: MScTI_PCA, MScTI_INTROHPC, C++, OS basics		
Literature: a reading list will be announced by the lecturer				
Form of Testing and Examination: 15 – 30 min. oral exam or 1h written exam, announced by lecturer				

Code: MScTI_FPGA		Course Title: FPGA Coprocessors		
Lecturer: Dr. A. Kugel		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture (2 hours) / practical exercise (lab, 1 hour avg) / project (lab, 1 hour avg) / homework • Just-in-time teaching sessions (4 hours) on selected topics 				
Objectives: After completing this course students are able to ...				
<ul style="list-style-type: none"> • list and explain advanced components of FPGA devices, • list and explain coprocessor architectures and communication types, • select, configure and program FPGA IP library elements, • create custom IP cores using structural data-flow and FSM based control-flow design techniques, • use IP cores to create hybrid applications for processor and reconfigurable coprocessor with appropriate interface mechanisms, • program and test coprocessor applications. 				
Contents:				
<ul style="list-style-type: none"> • Reconfigurable Computing Hardware <ul style="list-style-type: none"> ◦ FPGA Device Architecture and Features ◦ Reconfigurable Computing Architectures ◦ (Re-)Configuration Management • Programming Reconfigurable Systems <ul style="list-style-type: none"> ◦ Compute Models and System Architectures ◦ Programming FPGA Applications in VHDL ◦ Data- and Control- Flow Graphs ◦ High-Level Synthesis Tools • Mapping Designs to Reconfigurable Platforms <ul style="list-style-type: none"> ◦ Technology Mapping ◦ Datapath Optimizations • Projects: Implementing Applications with FPGAs • Computation, Image-processing, I/O oriented 				
Prerequisites: none		Recommended Knowledge: FPGA and HDL fundamentals (e.g. from MScTI_RES)		
Literature: Scott Hauck & André Dehon: Reconfigurable Computing; Morgan Kaufmann, 2008				
<p>Form of Testing and Examination: 50% score on exercises plus either oral exam (15min) or project (see below). Available option announced at start of course.</p> <p>Project exam: autonomous elaboration of project task assigned by lecturer. Successful completion requires all of: operational design/program, written report (5 pages), presentation (10 minutes) with colloquium, statement of unaided work.</p>				

Code: MScTI_HPNET		Course Title: High Performance Interconnection Networks		
Lecturer: Prof. Dr. U. Brüning		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (2 hours/week) • Exercise with homework (2 hours/week) 				
Objectives: The students ... <ul style="list-style-type: none"> • understand the concepts and principles of interconnection networks, • will be able to configure and use interconnection networks for given demands, • can use the learned structures to develop new high performance interconnection networks. 				
Contents: <ul style="list-style-type: none"> • Topologies, Switching, Routing, Flow Control • Fault tolerance and Deadlocks • Collective Communications • Congestion Management • Network Interfaces • On-Chip Networks • Performance Evaluation and Simulation 				
Prerequisites: none		Recommended Knowledge: MScTI_PCA, MScTI_APC		
Literature: a reading list will be provided in the script The script will be accessible on the web site of the Computer Architecture Group				
Form of Testing and Examination: 30' min. oral exam or 2h written exam, announced by lecturer				

The following module is imported from the master program in computer science:

Code: IPHR		Course Title: Parallel Algorithms, Application Computing (Paralleles Höchstleistungsrechnen)		
Lecturer: Prof. Dr. P. Bastian/ Dr. S. Lang		Type: Lecture with exercises		
Credit Points: 8	Workload: 240h	Teaching Hours: 6 / week	Language: German	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture 4 SWS • Exercises 2 SWS 				
Objectives: The student				
<ul style="list-style-type: none"> • knows different architectures for high-performance computers, • knows synchronization mechanisms in parallel systems including performance aspects, • can handle the most important programming paradigms for parallel systems, • is able to solve basic synchronization tasks, • understands the parallelization of linear algebra algorithms, • is able to assess the performance of a parallel program. 				
Contents:				
<ul style="list-style-type: none"> • Systems with global address space • Cache coherence • Systems with local address space and message passing • critical sections, condition synchronization, semaphore • posix threads • programming of graphics cards • message passing theory, MPI • client server model, remote procedure call • Assessment of parallel algorithms • load balancing • dense linear algebra algorithms, solution of sparse linear systems • particle methods • parallel sorting 				
Prerequisites: none		Recommended Knowledge: knowledge of a higher-level programming language (preferably C, C++), knowledge of data structures and algorithms		
Literature:				
Form of Testing and Examination: Minimum 50% successful exercises and final examination.				

The following module is imported from the master program in computer science:

Code: IPLGG		Course Title: Parallel Solution of Large Linear Systems (Parallele Lösung großer Gleichungssysteme)		
Lecturer: Prof. Dr. P. Bastian		Type: Lecture with exercises		
Credit Points: 8	Workload: 240h	Teaching Hours: 6 / week	Language: German	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture 4 SWS • Exercises 2 SWS 				
Objectives: The student <ul style="list-style-type: none"> • knows the discretization of scalar elliptic partial differential equations with the finite element method, • understands the abstract concept of subspace correction methods, • is able to apply this to domain decomposition and multigrid methods, • understands the convergence theory for these methods, • is able to implement these methods on a parallel system and can judge the performance of the methods. 				
Contents: <ul style="list-style-type: none"> • Basis of Finite Element methods for elliptic partial differential equations • Subspace correction methods • Overlapping and non-overlapping domain decomposition methods with convergence theory • Geometric multigrid methods with convergence theory • Algebraic multigrid methods 				
Prerequisites: none		Recommended Knowledge: knowledge of a higher-level programming language (preferably C++), knowledge of numerical methods for differential equations		
Literature:				
Form of Testing and Examination: Minimum 50% successful exercises and final examination.				

The following module is imported from the master program in physics:

Code: MWInf6 (Physics)		Course Title: Digital Image Processing		
Lecturer: Prof. Dr. B. Jähne		Type: Lecture with Exercise		
Credit Points: 8	Workload: 240h	Teaching Hours: 6 / week	Language: English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture (4h) • Exercise (2h) 				
Objectives: After the course the students will be able to: <ul style="list-style-type: none"> • judge what kind of processing needs to be applied to extract information of interest for a specific task from time series, images, and any kind of multidimensional signals and to apply it there knowledge to applications from natural sciences, life sciences and technology. 				
Contents: <ul style="list-style-type: none"> • Continuous and discrete signals, sampling theorem, signal representation • Fourier transform • Random variables and fields, probability density functions, error propagation • Homogeneous and inhomogeneous point operations • Neighborhood operations, linear and nonlinear filters, linear system theory • Geometric transformations and interpolation • Multi-grid signal presentation and processing • Averaging, edge and line detection, local structure analysis, local phase and wave numbers • Motion analysis in image sequences • Segmentation • Regression, globally optimal signal analysis, variation approaches, steerable and nonlinear filtering, inverse filtering • Morphology and shape analysis, moments, Fourier descriptors • Bayesian image restoration • Object detection and recognition 				
Prerequisites: none		Recommended Knowledge: UKInf1 (Physics)		
Literature: B. Jähne, Digital Image Processing, 6th edition, Springer 2005				
Form of Testing and Examination: Written examination				

The following module is imported from the master program in physics:

Code: MWInf5 (Physics)		Course Title: Physics of Imaging		
Lecturer: Prof. Dr. B. Jähne		Type: Lecture		
Credit Points: 4	Workload: 120h	Teaching Hours: 4 / week	Language: English	Term: SS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> Lecture 				
Objectives: The students				
<ul style="list-style-type: none"> learn the basics of the Physics of Imaging; common principles and techniques of imaging for atomic to astronomical scales. They will be enabled to analyse imaging measuring tasks from all kind of application areas and select a proper imaging method, wavelength of radiation, and lenses. 				
Contents:				
<ul style="list-style-type: none"> Projective geometry, optics, wave optics, Fourier optics and lens aberrations Radiometry of imaging Methods of imaging: scanning electron microscopy, X-ray, EDX, FLIM, FRET, fluorescence imaging, near-field imaging CCD and CMOS technology Holography, ultrasound imaging, CT- computer tomography, magnetic resonance imaging... Satellite imaging, synthetic aperture radar, radio astronomy 				
Prerequisites: none		Recommended Knowledge: UKInf2, PEP1-PEP4		
Literature: B. Jähne, Digital Image Processing, 6th edition, Springer 2005				
Form of Testing and Examination: Oral examination				

Code: MScTI_MIS		Course Title: Modern Image Sensors		
Lecturer: Prof. Dr. B. Jähne		Type: Lecture		
Credit Points: 2	Workload: 60h	Teaching Hours: compact course	Language: English	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> Lecture 				
Objectives: The students				
<ul style="list-style-type: none"> learn all the basic knowledge about image sensors one should know to apply image processing techniques successfully and then be able to choose the right image sensor for a given imaging application. 				
Contents:				
<ul style="list-style-type: none"> Radiation detection: Basic principle of quantum detectors, quantum efficiency and responsivity, dark signal, overall system gain, spectral sensitivity; Non-silicon solid-state imaging: InGaAs, HgCdTe, InSb, QWIP, superlattice detectors; indirect (thermal) detectors: pyroelectricity, microbolometers Imaging detectors: the charge-coupled device (CCD), CCD sensor architecture, frame transfer, interline transfer, electronic shutter, microlens arrays; CMOS imaging sensors and active pixels, scientific CMOS sensors, color and spectral sensors, high-speed imaging, artefacts of image sensors Standard interfaces for digital cameras: Camera Link, Camera Link HS, CoaxPress (CXP), Firewire (IEEE1394), USB2, USB3 Vision, GigE Vision, towards a standardized interface: GenICam Performance characterization for image sensors: EMVA 1288 standard, noise model for a linear camera, photon transfer method, signal to noise ratio (SNR), signal saturation, absolute sensitivity threshold, dynamic range (DR), Dark current and auto-saturation time, Spatial nonuniformities and defective pixel: spatial variances, spectrogram method, logarithmic histograms, profiles Practical issues: Measuring equipment for camera performance characterization, Application-oriented camera selection according to different criteria 				
Prerequisites: none		Recommended Knowledge: BSc Applied Computer Science		
Literature: will be given at course				
Form of Testing and Examination: Oral examination				

The following module is imported from the master program in physics:

Code: MWInf7 (Physics)		Course Title: Pattern Recognition		
Lecturer: Prof. Dr. F. Hamprecht		Type: Lecture with exercises		
Credit Points: 8	Workload: 240h	Teaching Hours: 6 / week	Language: English	Term: SS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture • Exercise 				
Objectives: The students				
<ul style="list-style-type: none"> • given a huge bunch of data, find out what's in it; build automated diagnostic systems or expert systems that automatically learn to make reliable predictions from a training set of examples. Lectures and exercises will be interwoven and allow you to build such systems by yourself; real-life examples will be drawn from for the application areas named below. 				
Contents:				
<ul style="list-style-type: none"> • Curse of dimensionality • Variable selection and dimension reduction for high-dimensional data • Unsupervised learning: Cluster analysis • Supervised learning: Regression • Supervised learning: Classification by means of neural networks, support vector machines, etc. • Graphical models • Applications: Data mining, industrial quality control, process monitoring, astrophysics, medicine, life sciences 				
Prerequisites: none		Recommended Knowledge: UKInf1, Knowledge about Linear Algebra, Probability, Statistics		
Literature: Pattern Classification (2nd ed.) by Richard O. Duda, Peter E. Hart and David G. Stork. Wiley, 2000.				
Form of Testing and Examination: Defined at lecture start				

Code: MScTI_ASYSTHEO		Course Title: Advanced System Theory		
Lecturer: Prof. Dr.sc.techn. E. Badreddin		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture: 2 hours/week • Exercise: 2 hours/week 				
Objectives: After completing this course the students will be able to: <ul style="list-style-type: none"> • describe and analyze systems by using fuzzy sets, artificial neural networks or game theory and • design control systems for some relevant examples based on the aforementioned methods. 				
Contents: The course consists of three parts: <p>Fuzzy sets and fuzzy control</p> <ul style="list-style-type: none"> • Definitions, operations and relations with fuzzy sets • Linguistic modelling of dynamic systems • Linguistic system analysis and control <p>Artificial Neural Networks</p> <ul style="list-style-type: none"> • Introduction, motivation and definitions • Modelling of neural networks • Processing artificial neural networks • Application of artificial neural networks in control systems <p>Game Theory</p> <ul style="list-style-type: none"> • Introduction (motivation, definitions, description of a game) • Strategic games and extensive games • Differential games • Application of game theory in control systems 				
Prerequisites: none		Recommended Knowledge: System Theory, Theory of linear systems		
Literature: Y. C. Shin and C. Xu Intelligent Systems: Modeling, Optimization, and Control. CRC Press, 2008. J. Engwerda, LQ Dynamic Optimization and Differential Games. J. Wiley, 2005.				
Form of Testing and Examination: 30' oral exam				

Code: MScTI_AUTO		Course Title: Design of Autonomous Systems		
Lecturer: Prof. Dr.sc.techn. E. Badreddin		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture: 2 hours/week • Exercise: 2 hours/week 				
Objectives: After completing this course the students will be able to: <ul style="list-style-type: none"> • understand the design requirements and formulate the system specifications, • formulate the problem, • decompose the problem of the design of autonomous mechatronic systems into sub-problems, • choose an architecture in which the solutions of the sub-problems leads to the overall design, • find an implementation to each of the posed sub-problems, • integrate the individual implementation into the selected overall system architecture, • understand the practical constraints and requirements of the real-time realization and • demonstrate the whole design process by using Autonomous Wheeled-mobile robots. 				
Contents: <ul style="list-style-type: none"> • Introduction to autonomous systems • The design problem • Control structure • Behavior fusion • Kinematics • Kinematic control • Collision avoidance • Navigation • Higher-level behavior • Implementation • Overall system specifications 				
Prerequisites: none		Recommended Knowledge: System Theory, theory of linear systems (Signals and Systems 1)		
Literature: Badreddin, E., "Control and System Design of Wheeled Mobile Robots", Habilitationsschrift, 1997. Dudek, G., Jenkin, M., Computational Principles of Mobile Robotics, Cambridge University Press, 2000.				
Form of Testing and Examination: 30' oral exam				

Code: MScTI_DIGCTRL		Course Title: Digital Control in Real Time		
Lecturer: Prof. Dr.sc.techn. E. Badreddin		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture: 2 hours/week • Exercise: 2 hours/week 				
Objectives: After completing this course the students will be able to: <ul style="list-style-type: none"> • understand the sampling process, • use the z-transform for solving control problems, • model, analyze and design digital control systems and • implement a control system in real time. 				
Contents: <ul style="list-style-type: none"> • Introduction to digital signals and systems • Sampling theory and z-Transform • Modelling of discrete-time dynamic systems • Analysis of discrete-time dynamic systems • Digital design of control systems • Real-time implementation • Practical aspects of the implementation and operation 				
Prerequisites: none		Recommended Knowledge: System Theory, Theory of linear systems		
Literature: K. J. Aström and B. Wittenmark, Computer-controlled systems. Prentice Hall, 1997. M. S. Fadali and A. Visioli, Digital Control Engineering: Analysis and Design. Academic Press, 2009				
Form of Testing and Examination: 30' oral exam				

Code: MScTI_DAQ		Course Title: Data Acquisition		
Lecturer: Prof. Dr.sc.techn. E. Badreddin		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Lecture: 2 hours/week • Exercise: 2 hours/week 				
Objectives: After completing this course the students will be able to: <ul style="list-style-type: none"> • understand the components of a Data Acquisition System (DAS), • describe the basic principles, which dominates each stage of a DAS and • implement a DAS for a given application. 				
Contents: <ul style="list-style-type: none"> • Overview of Sensors and Signals • Analogue and digital inputs and outputs • Hardware and software for data acquisition • Signal conditioning • Data communication and data transfer • Signal processing 				
Prerequisites: none		Recommended Knowledge: Digital control in real time, Sensor technologies		
Literature: J. Park and S. Mackay Practical Data Acquisition for Instrumentation and Control Systems (IDC Technology). Newnes, 2003. H. R. Taylor, Data Acquisition for Sensor Systems. Springer, 2010.				
Form of Testing and Examination: 30' oral exam				

Code: MScTI_RELSYS		Course Title: Design of Reliable and Dependable Systems		
Lecturer: Prof. Dr.sc.techn. E. Badreddin		Type: Lecture with exercises		
Credit Points: 6	Workload: 180h	Teaching Hours: 4 / week	Language: German / English	Term: WS
Module Parts and Teaching Methods:				
<ul style="list-style-type: none"> • Lecture: 2 hours/week • Exercise: 2 hours/week 				
Objectives: After completing this course the students will be able to:				
<ul style="list-style-type: none"> • understand und describe the basic properties of reliable and dependable systems, • utilize problem specific modelling methods to describe a variety of systems, • analyze technical systems with respect to their reliability and dependability, • apply methods and techniques for the design and realization of reliable and dependable systems. 				
Contents:				
<ul style="list-style-type: none"> • Introduction • Reliability and Dependability Measures • Boolean Reliability Modelling • Markov Modelling • Petri Nets • Redundancy Techniques for Hardware and Software • Fault Injection • Description of Dynamic Systems • System Architectures and Topologies • Dynamic Safety Control • Monitoring and Diagnosis • Human Reliability • System Design Methods and Techniques • Application Examples from Mobile Robotics, Avionics, and Medical Technology 				
Prerequisites: none		Recommended Knowledge: Theory of linear systems (Signals and Systems 1)		
Literature:				
M. Walter, W. Schneeweiss, „The Modeling World of Reliability/Safety Engineering“, LiLoLe-Verlag GmbH, Hagen, Germany, 2005. Uwe Kay Rakowsky, “System-Zuverlässigkeit“, Hagen/Westfalen: LiLoLe-Verlag, 2002. Paperback, 444 Seiten, ISBN 3-934447-22-8.				
Form of Testing and Examination: 30' oral exam				

Code: MScTI_SEM		Course Title: Advanced Seminar		
Lecturer: all groups		Type: Seminar with Presentation		
Credit Points: 4 + 2 (soft skills)	Workload: 180h	Teaching Hours: 2 / week	Language: German, maybe English	Term: WS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Seminar 				
Objectives: After this course the students will be able to: <ul style="list-style-type: none"> • Search literature for a specific subject, • Select subject / material for a presentation, • Prepare material (slides) for a presentation, • Give a scientific presentation. 				
Contents: <ul style="list-style-type: none"> • Literature research • Preparation of presentation • Oral Presentation (~45 Minutes) • Preparation of a short summary report (~10 pages) • Active participation in other student's presentations & discussion 				
Prerequisites: none		Recommended Knowledge: General Knowledge about the chosen field		
Literature: Partially provided by lecturer				
Form of Testing and Examination: Presentation, Written Summary, Regular active participation				

Code: MScTI_SA		Course Title: Student Research Project		
Lecturer: all groups		Type: Practice Course		
Credit Points: 14	Workload: 420h	Teaching Hours: n.a.	Language: n.a.	Term: WS/SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Practical Course 				
Objectives: After this course the students will be able to: <ul style="list-style-type: none"> • Dig into scientific and technical aspects of a selected topic, • Manage and carry through a small research project, • Write a medium length report. 				
Contents: <ul style="list-style-type: none"> • Research work on a specific topic. • Management of work. • Preparation of a medium length report. 				
Prerequisites: none		Recommended Knowledge: Knowledge in research field		
Literature: Depending on subject. Provided by supervisor				
Form of Testing and Examination: Written Report				

Code: MScTI_THESIS		Course Title: Master Thesis		
Lecturer: all groups		Type: Practice Course		
Credit Points: 30	Workload: 900h	Teaching Hours: n.a.	Language: n.a.	Term: WS/SS
Module Parts and Teaching Methods: <ul style="list-style-type: none"> • Master Thesis 				
Objectives: After this course the students will be able to: <ul style="list-style-type: none"> • Manage and carry through a large research project, • Write an extended thesis, • Report on own scientific work in an oral presentation. 				
Contents: <ul style="list-style-type: none"> • Research work on a specific topic. • Management of work. • Preparation of a longer written thesis. • Oral presentation in the Colloquium. 				
Prerequisites: none		Recommended Knowledge: Knowledge in research field		
Literature: Depending on subject. Provided by supervisor				
Form of Testing and Examination: Written Thesis, Colloquium				